

Design Rules Verification Report

Filename : C:\Users\bhuffin\OneDrive - Qorvo\Desktop\Altium 4000 & SolidWorks 4500 docs\EVB Presubmissions template Rev D\CQ-11129_RFFM5648-40001_M.Zhu_6-8-23\001\Design\QDN1SGLB-40001.PcbDoc

Warnings 0
Rule Violations 0
Waived Violations 2

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=78.74mil) (InNet('HS_Source')), (InNet('LS_Source'))	0
Clearance Constraint (Gap=10mil) (All), (All)	0
Clearance Constraint (Gap=78.74mil) (InNet('SWITCH_NODE')), (InNet('DC_GND') or InNet('LS_Source'))	0
Clearance Constraint (Gap=78.74mil) (InNet('VDC') or InNet('LS_Drain') or InNet('Rsnub') or InNet('NetR12_1') or	0
Short-Circuit Constraint (Allowed=No) (All), (All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.1mil) (Max=196.85mil) (Preferred=19.685mil) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) (All)	0
Minimum Annular Ring (Minimum=5mil) (All)	0
Hole Size Constraint (Min=0.1mil) (Max=500mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All), (All)	0
Minimum Solder Mask Sliver (Gap=1mil) (All), (All)	0
Silk To Solder Mask (Clearance=1mil) (IsPad), (All)	0
Silk to Silk (Clearance=0.039mil) (All), (All)	0
Net Antennae (Tolerance=0mil) (All)	0
Height Constraint (Min=0mil) (Max=3149.606mil) (Preferred=500mil) (All)	0
Total	0

Waived Violations	
Un-Routed Net Constraint (All)	2
Total	2

Un-Routed Net Constraint (All)	
Un-Routed Net Constraint: Net VDC Between Pad C46-1(1864.174mil,2531.497mil) on Top Layer And Pad C34-1(2864.174mil,2531.497mil) on Top Layer	
Un-Routed Net Constraint: Net SWITCH_NODE Between Pad C37-2(2035.434mil,3559.056mil) on Top Layer And Pad C45-2(2704.725mil,3559.056mil) or	