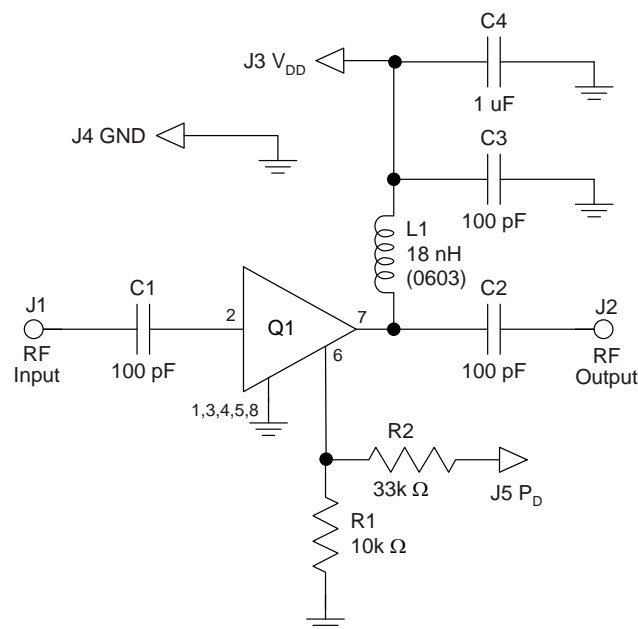
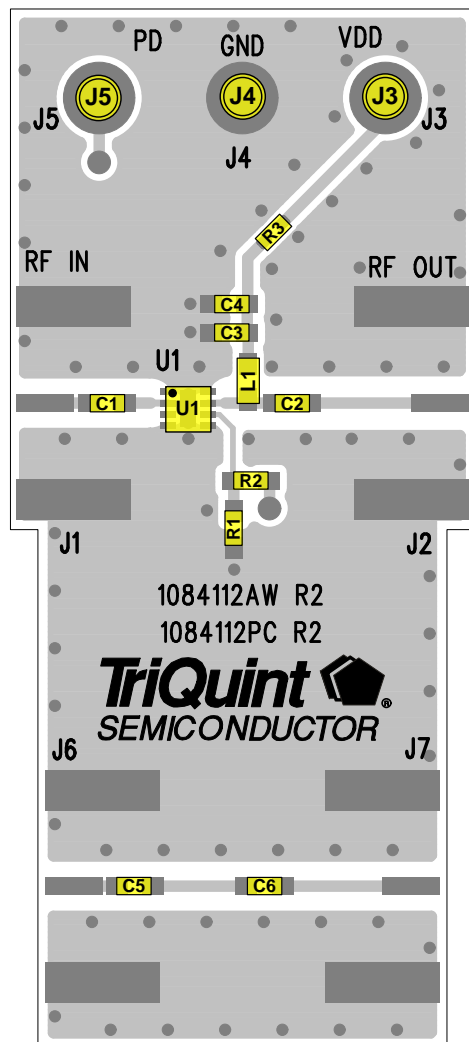


TQP3M9037

Ultra Low Noise, High Linearity LNA



Evaluation Board TQP3M9037-PCB



Notes:

1. See PC Board Layout, page 5 for more information.
2. R3 (0 Ω jumper) is not shown on the schematic and may be replaced with copper trace in the target application layout.
3. All components are of 0402 size unless stated on the schematic.
4. C1, C2, and C3 are non-critical values. The reactive impedance should be as low as possible at the frequency of operation for optimal performance.
5. The L1 value is non-critical and needs to provide high reactive impedance at the frequency of operation.
6. R1 and R2 are optional and do not need to be loaded if the shut-down functionality is not needed; i.e. FDD applications. If R1 and R2 are not loaded, the LNA will operate in its standard "ON" state.
7. A through line is included on the evaluation board to de-embed the board losses.

Bill of Material

Reference Des.	Value	Description
U1	n/a	TQP3M9037 Ultra Low Noise Amplifier
R1	10K Ω	Resistor, Chip, 0402, 5%, 1/16W
R2	33K Ω	Resistor, Chip, 0402, 5%, 1/16W
R3	0 Ω	Resistor, Chip, 0402, 5%, 1/16W
L1	18 nH	Inductor, 0603, 5%, Ceramic
C4	1.0 μ F	Cap., Chip, 0402, 10%, 10V, X5R
C1, C2, C3, C5, C6	100 pF	Cap., Chip, 0402, 5%, 50V, NPO/COG
J3, J4, J5	n/a	Solder Turret